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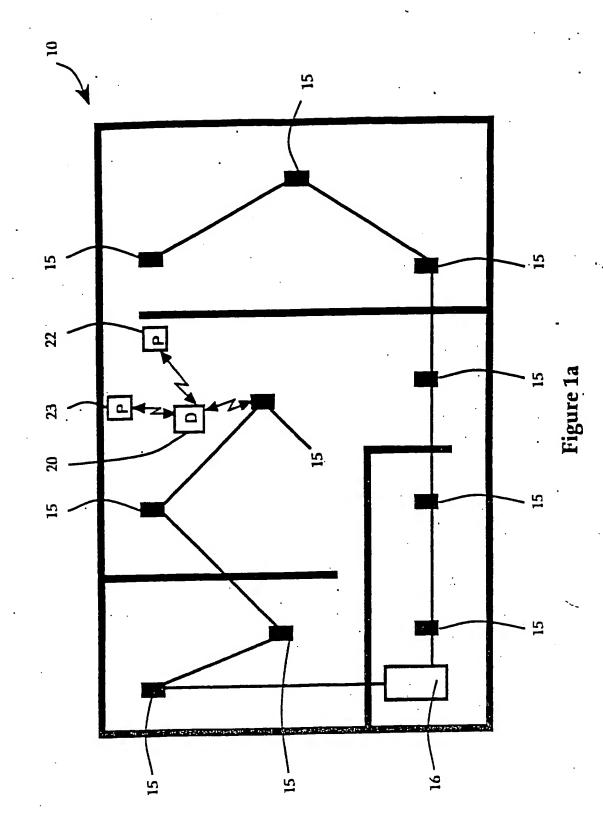
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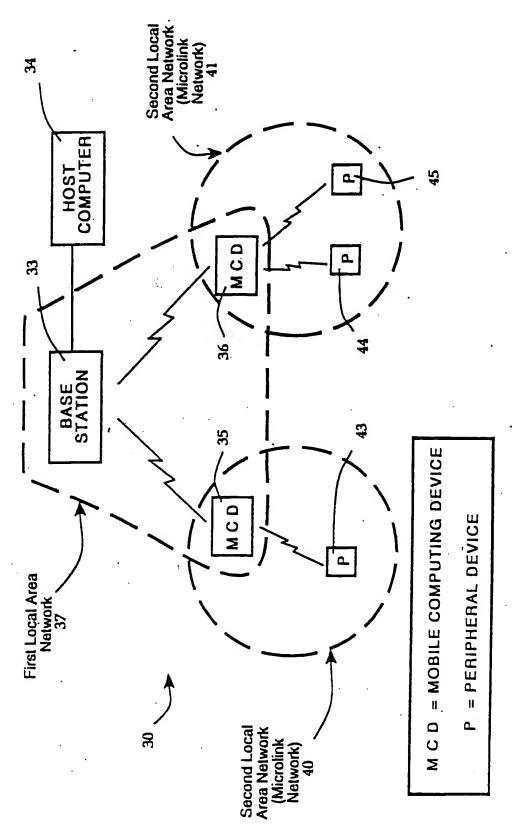


Figure 1b

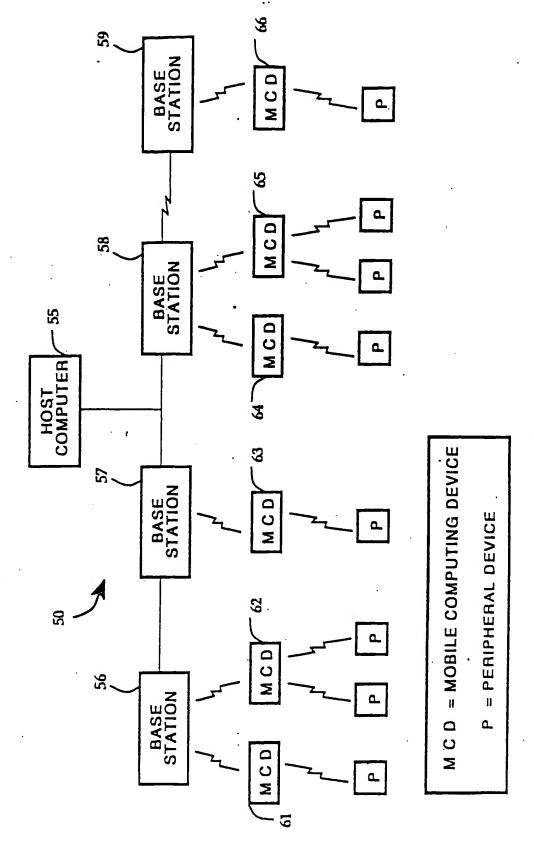


Figure 1c

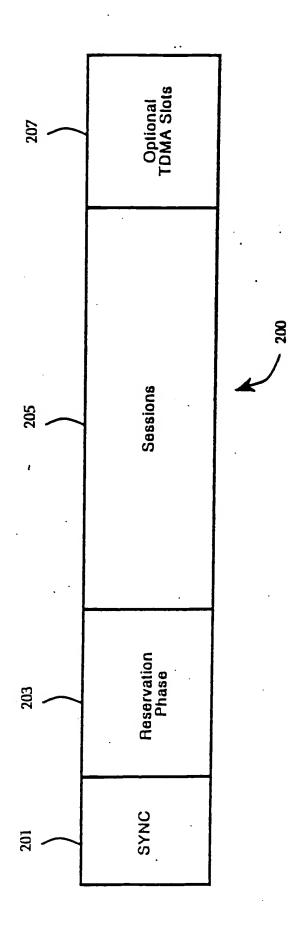


Figure 2

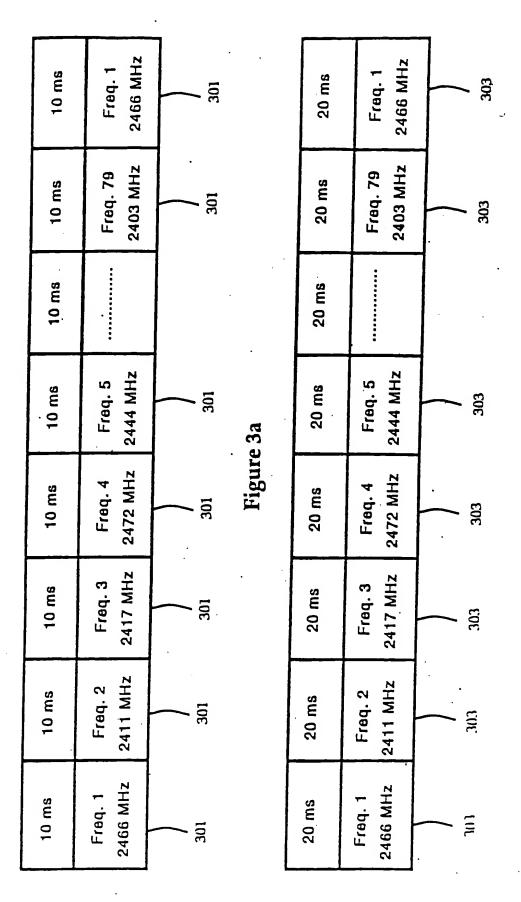


Figure 3b

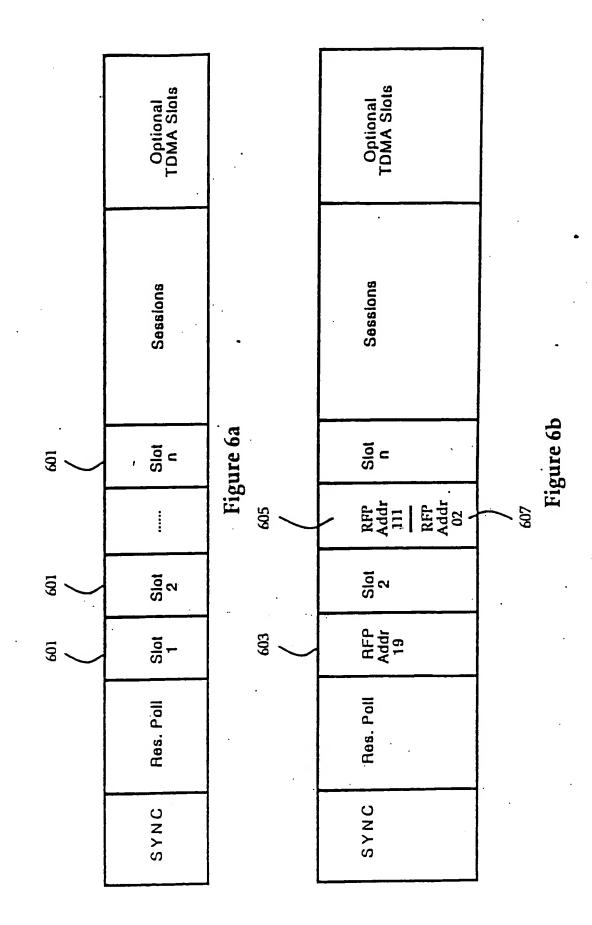
Freq 1: 2	Freq 1: 2466 MHz	Freq 2: 2	eq 2: 2411 MHz	Freq 3: 2417 MHz	417 MHz	Freq 5:	Freq 5: 2444 MHz
AI. 1	AI. 2	Al. 1	AI. 2	AI. 1	AI. 2	A	· ·
20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	3.IV
						£0 1113	SIII 07

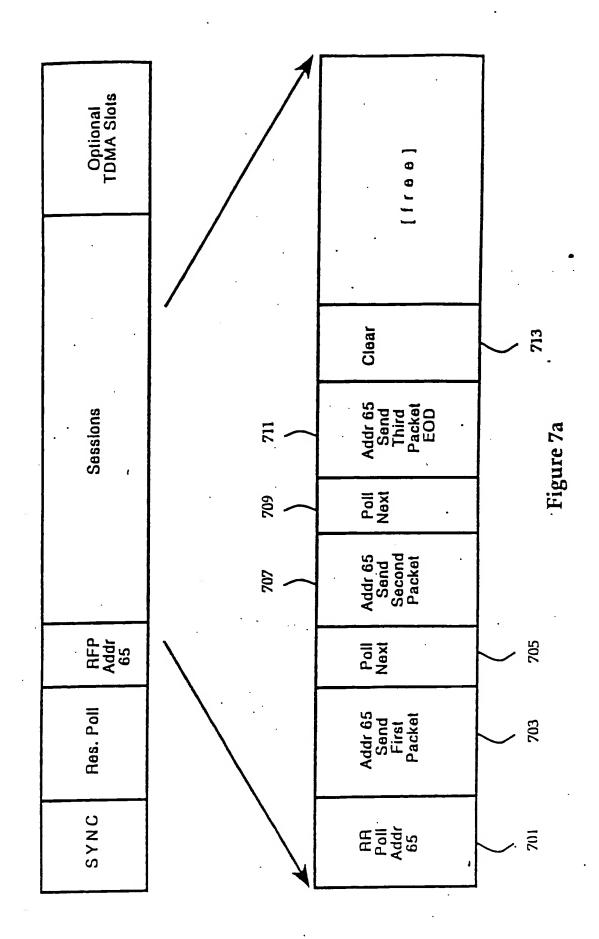
Figure 4a

Freq 2: 2411 MHz I. 2 Al. 3 Al. 4 I. ms 20 ms 20 ms	
411 MHz Al. 3 20 ms	
5	
Freq 2: Al. 2 20 ms	
Al. 1 20 ms	
Al. 4 20 ms	
6 MHz Al. 3 20 ms	
Freq 1: 2466 MHz Al. 2 Al. 20 ms 20 n	
Al. 1 20 ms	; /

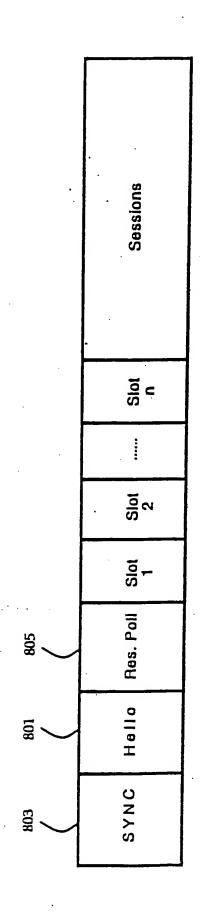
Figure 4b

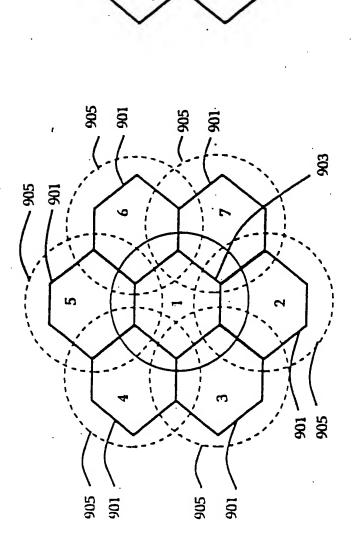
Figure 5b



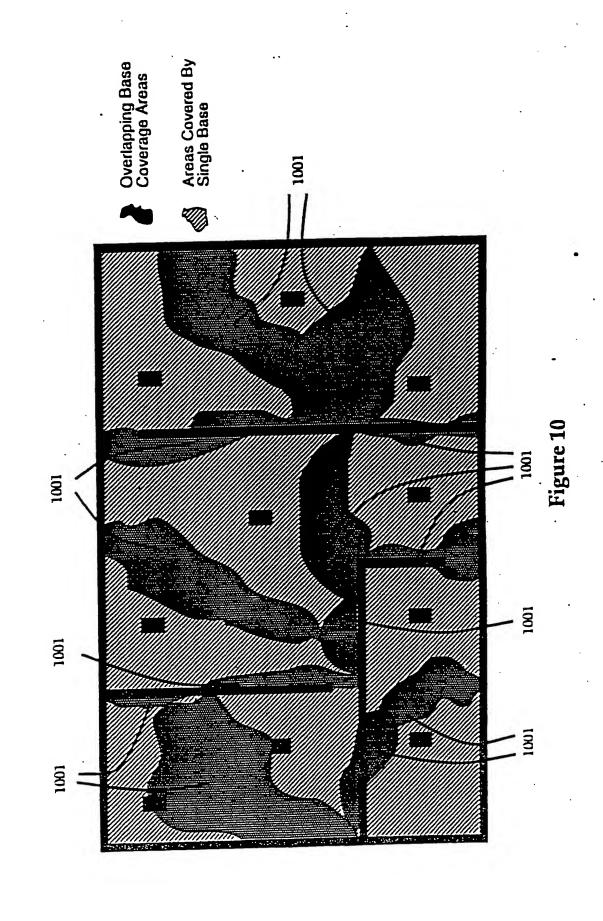


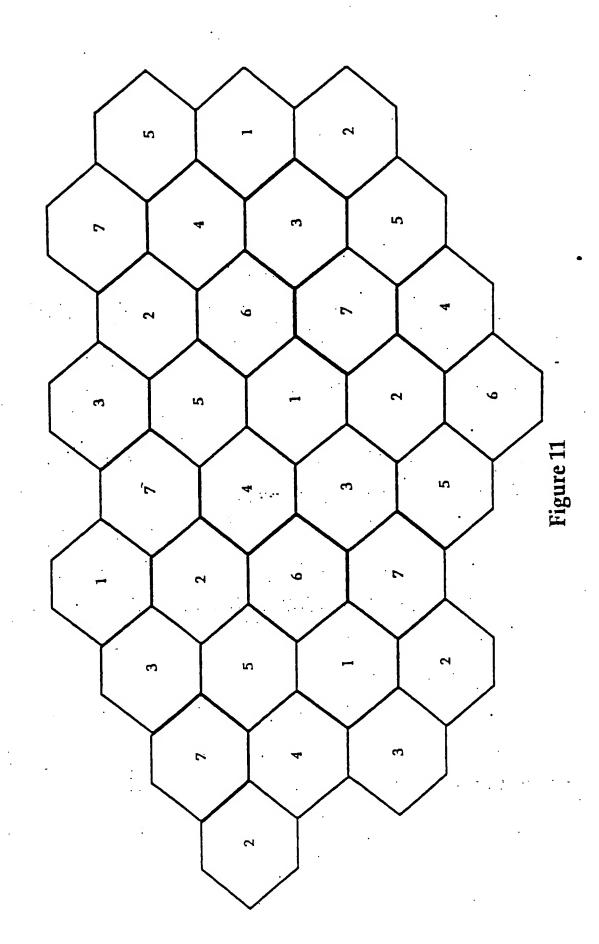
	Optional TDMA Slots		[free]	
		723	Clear	
	S.	·	Addr 103 Clear	
	Sessions	·	CP Send Second Packet EOD	Figure 7b
		·	Addr 103 Poli Next	
	RFP Addr 65		CP Send First Packet	
	Res. Poll			
·	Re		Addr 103 Poll CP	
*	SYNC		RR Poll RFP Addr 103	721





, 907





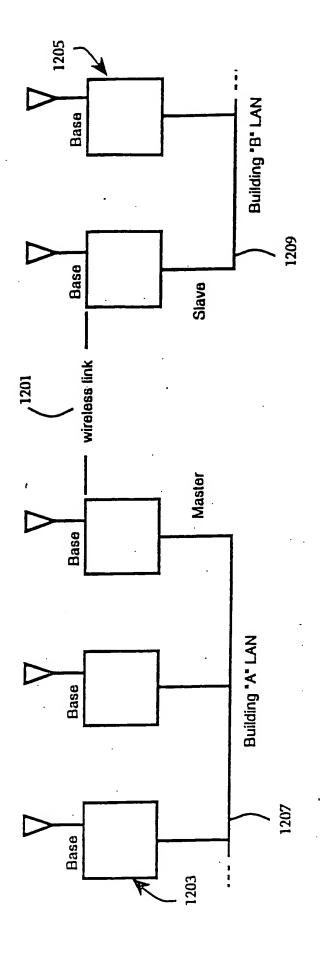


Figure 12

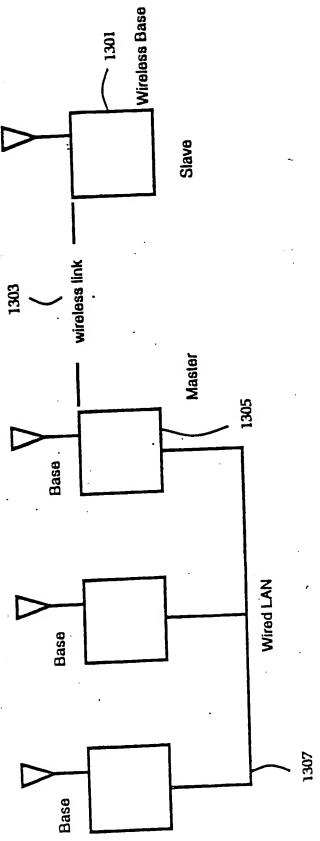
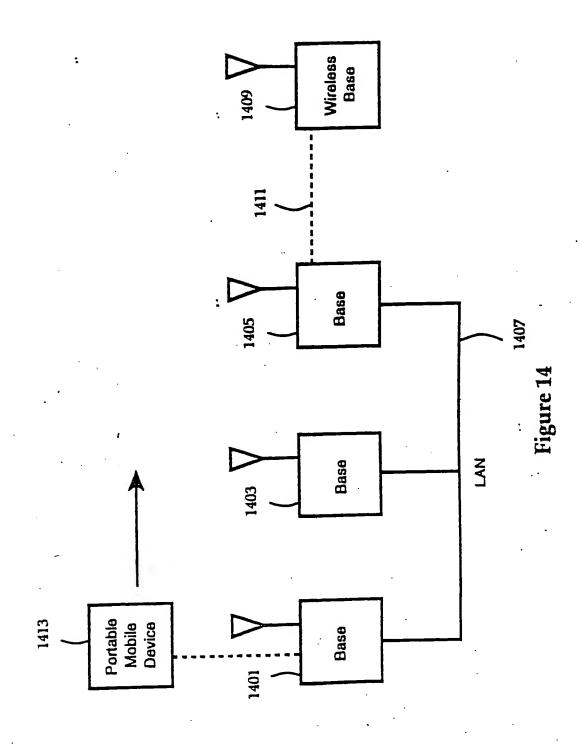


Figure 13



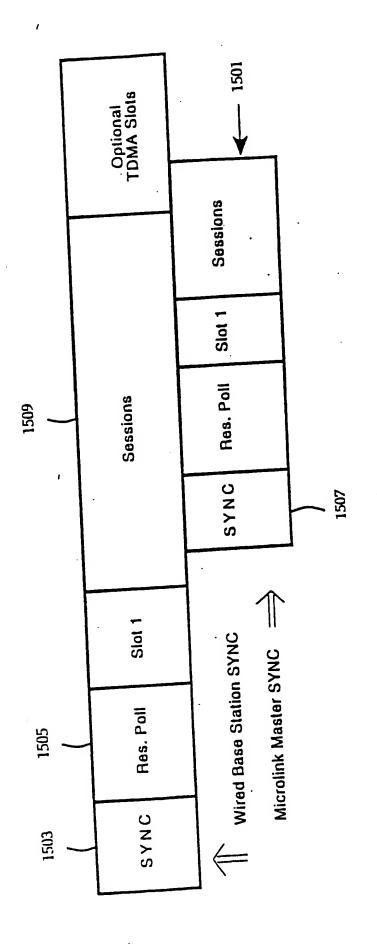


Figure 15

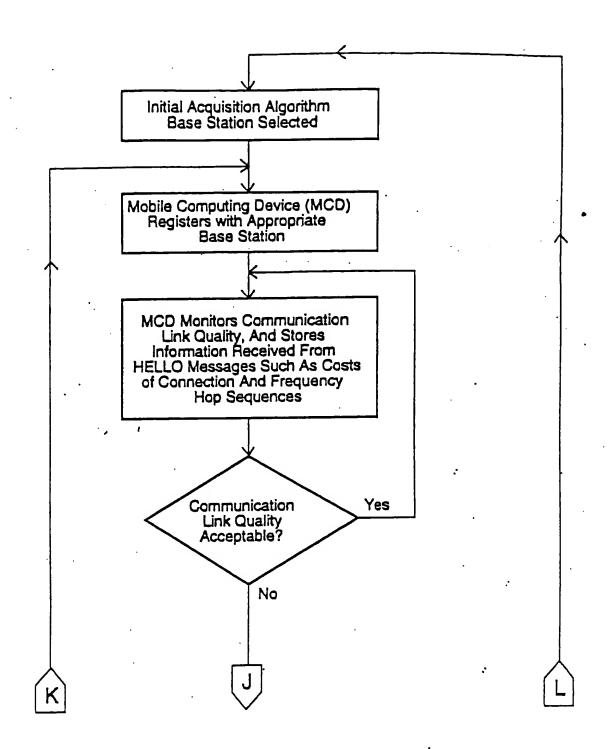
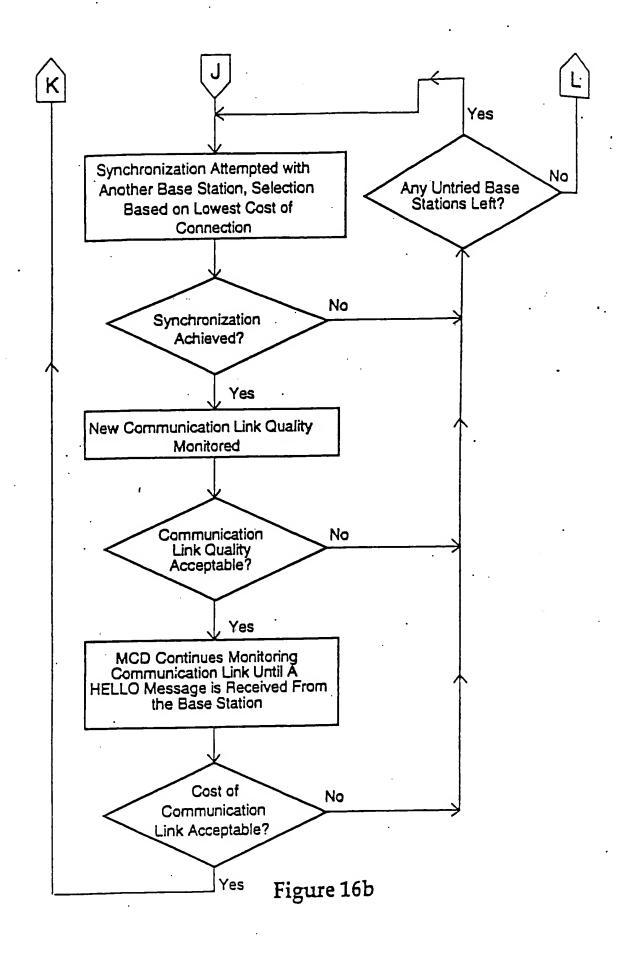


Figure 16a



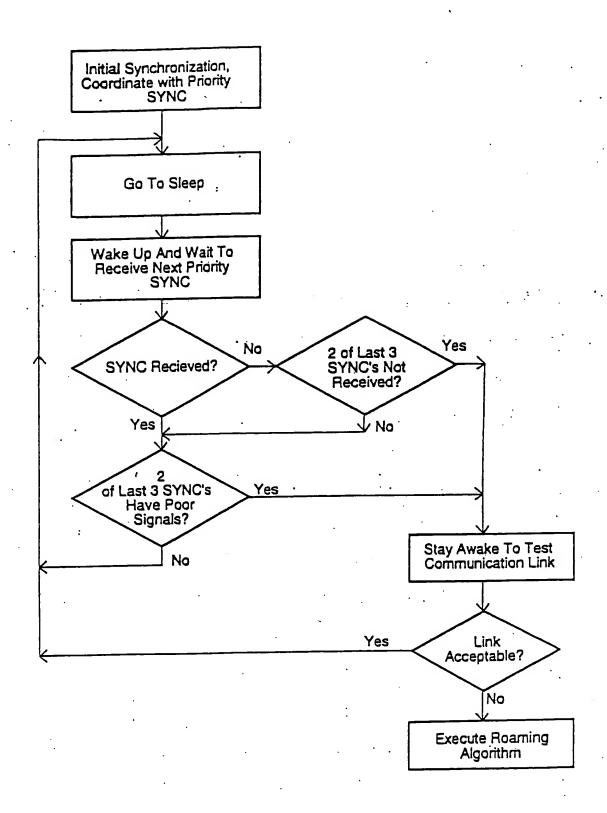
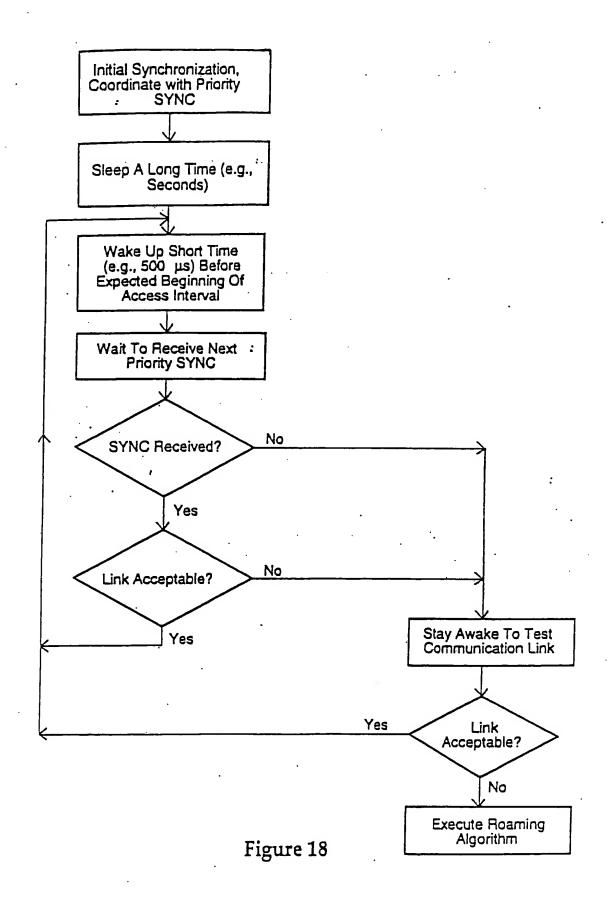
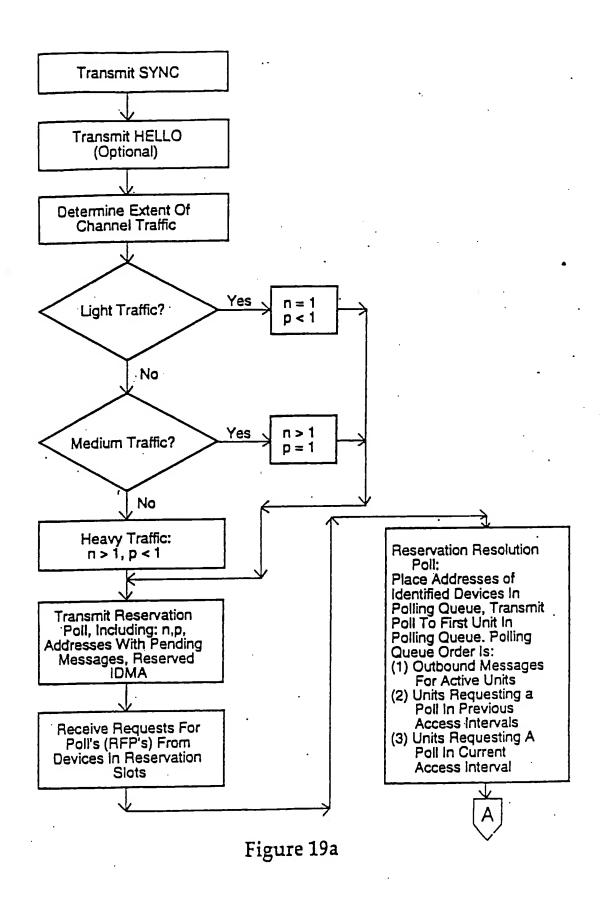


Figure 17





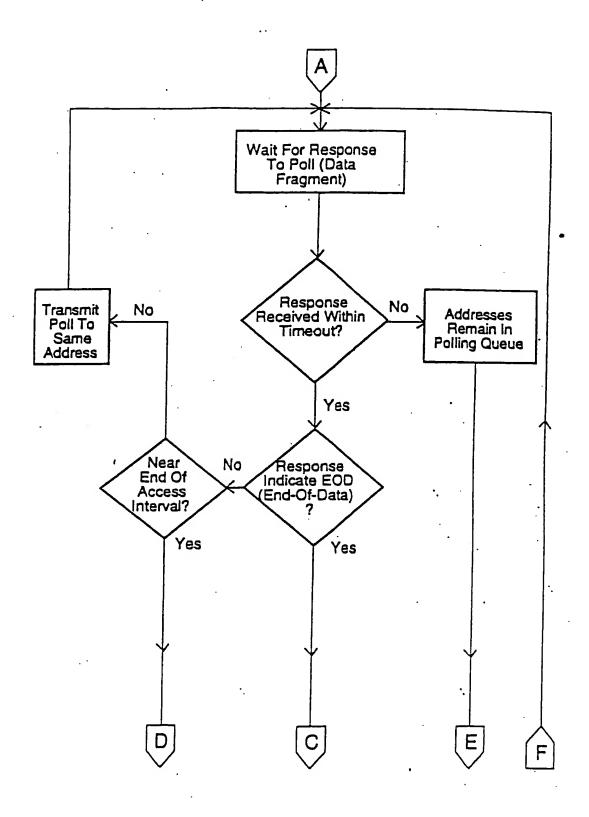


Figure 19b-1

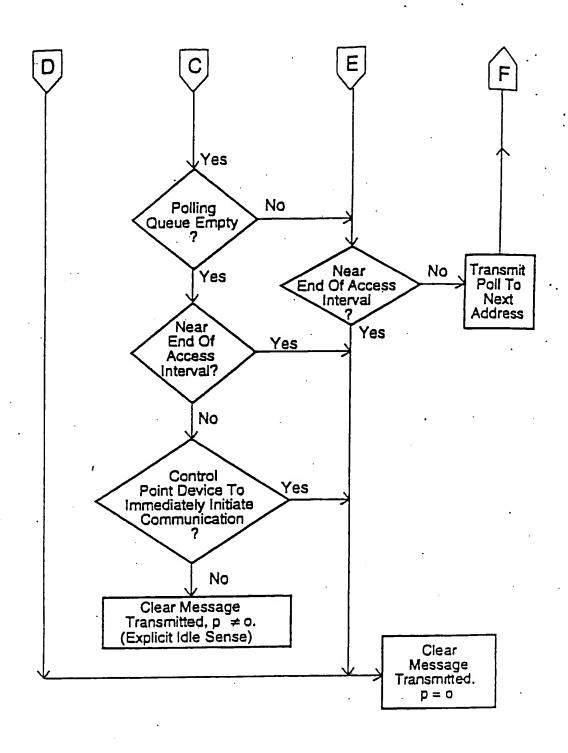


Figure 19b-2

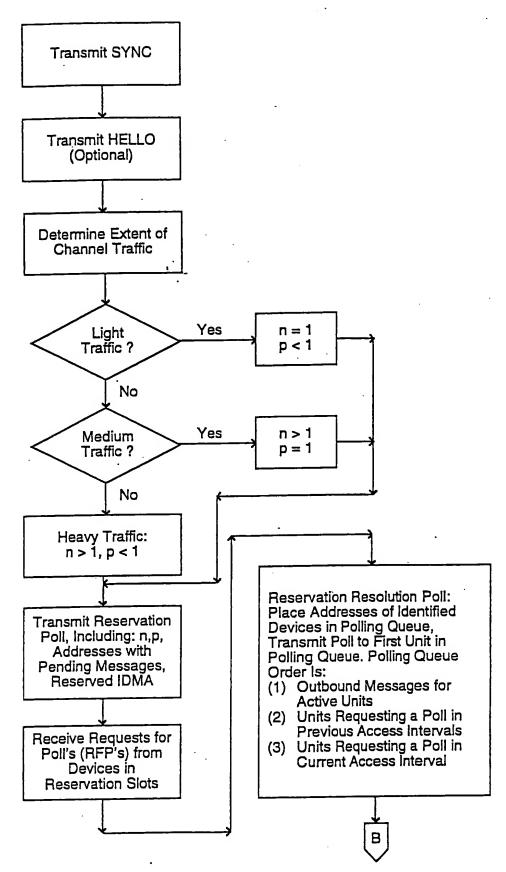


Fig. 20a

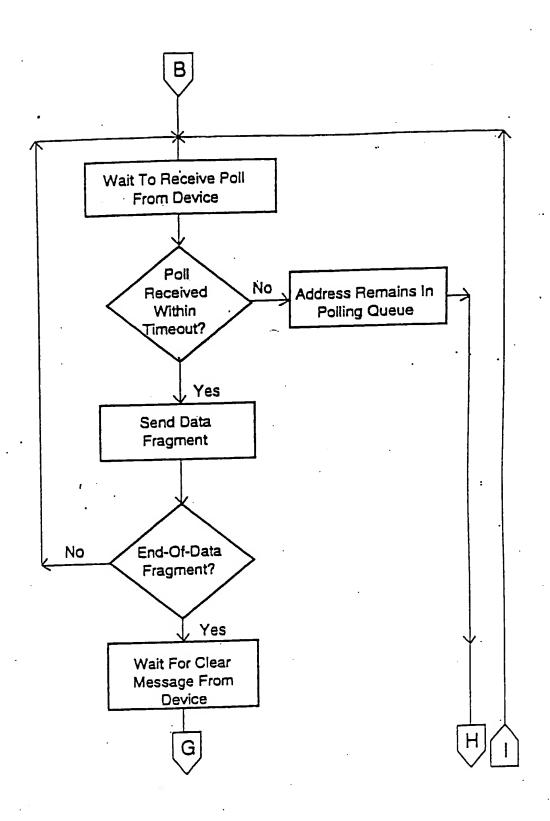


Figure 20b-1

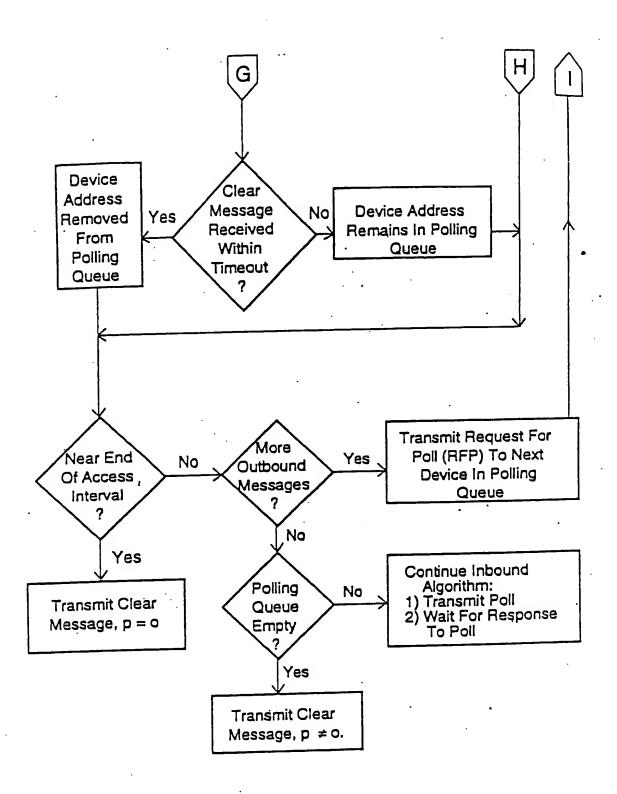
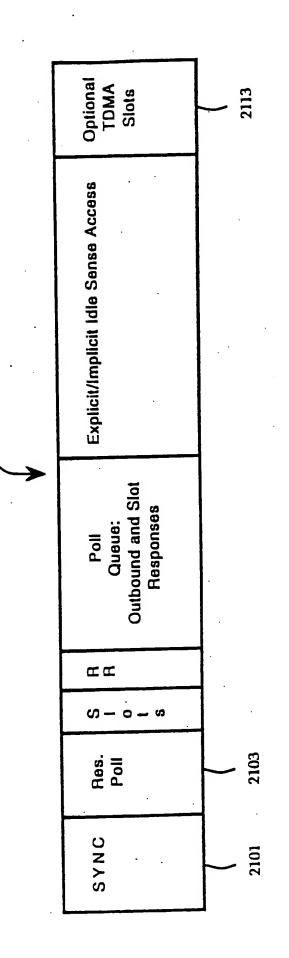


Figure 20b-2



2100

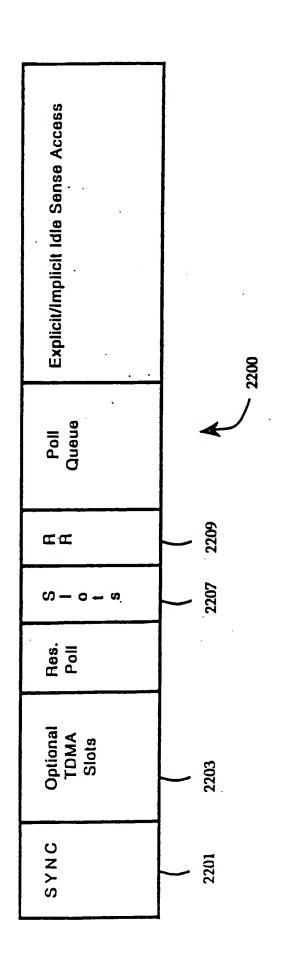


Figure 22

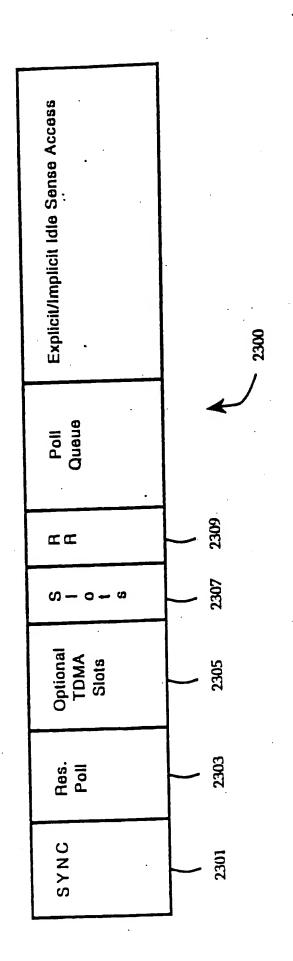


Figure 23

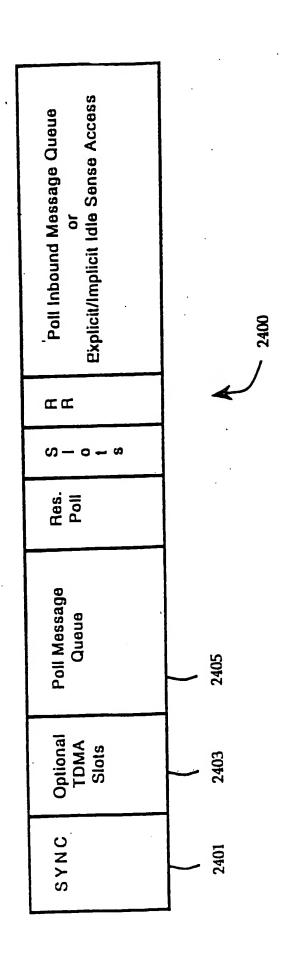
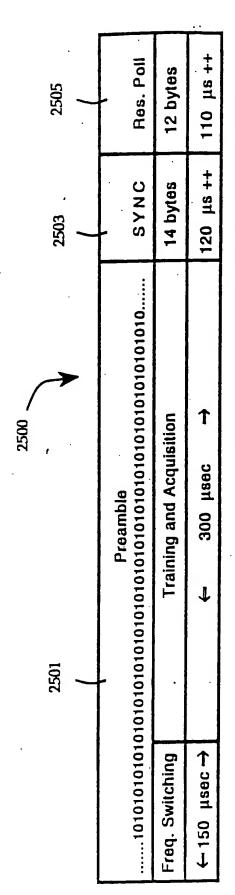


Figure 24



++ allows additional bit times for HDLC zero bit insertion

		CONT.
Вуте	Content	Definition
-	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _H)
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority Sync, or Priority SYNC with
		HELLO
2	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	Ol 19N
6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10•	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Stots
		3 Bits Number of Available Time Stots
11.	Random	Randomly Selected Character for Improved Encountion
12	CRC 1	Error Check
13	CRC 2	Error Check
14	FLAG	Frame Delimiter

Figure 26

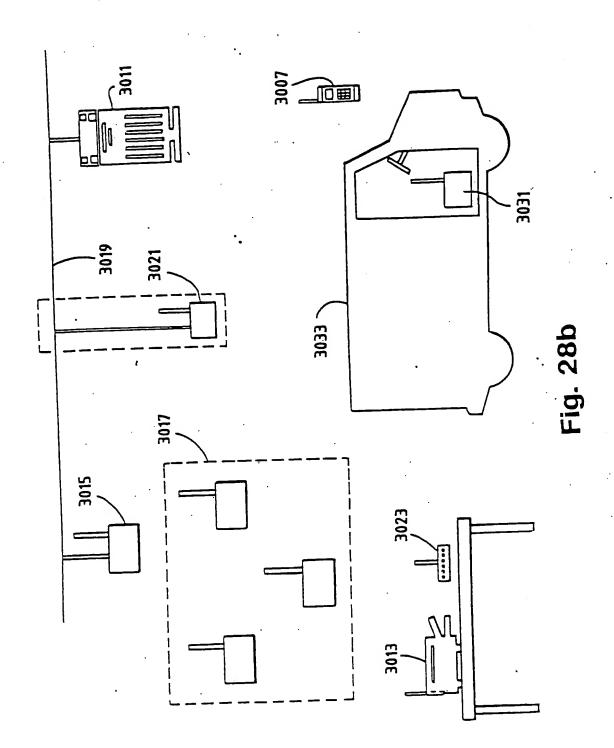
*Optionally Encrypted Bytes

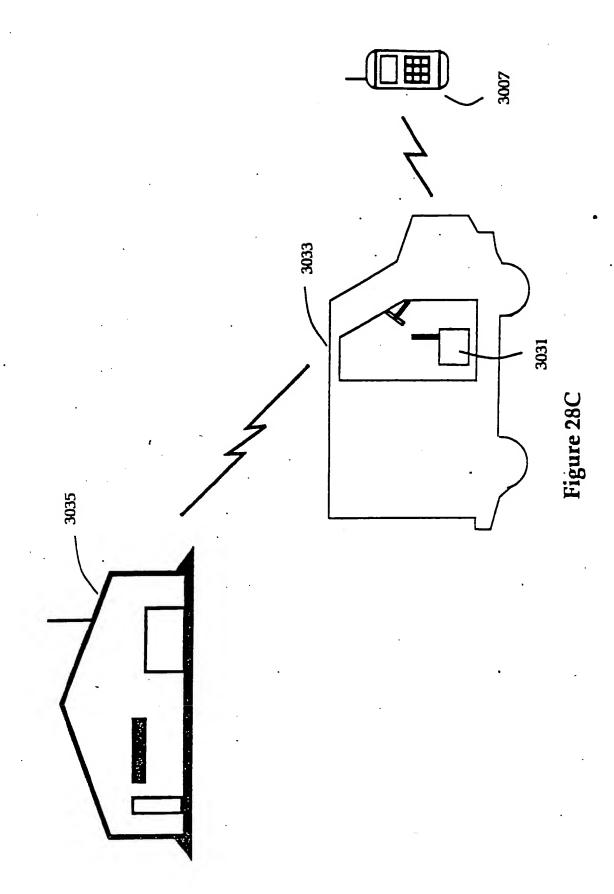
Reservation Poll

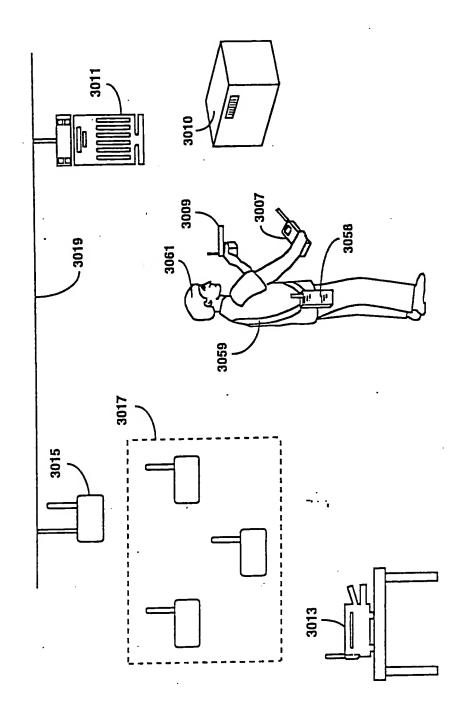
Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFF_H)	Destination Address, 2nd Byte	Message Type = RES POLL	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Sequence ID	Hopping Sequence Index		3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved	Error Check	Error Check	Frame Delimiter
Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	SLOT PARMS		Sequence	Index	NET PARMS				CRC 1	CRC 2	FLAG
Byte	-	2	. 6	4	5	9	7.	₽\$	•6				10	=	12

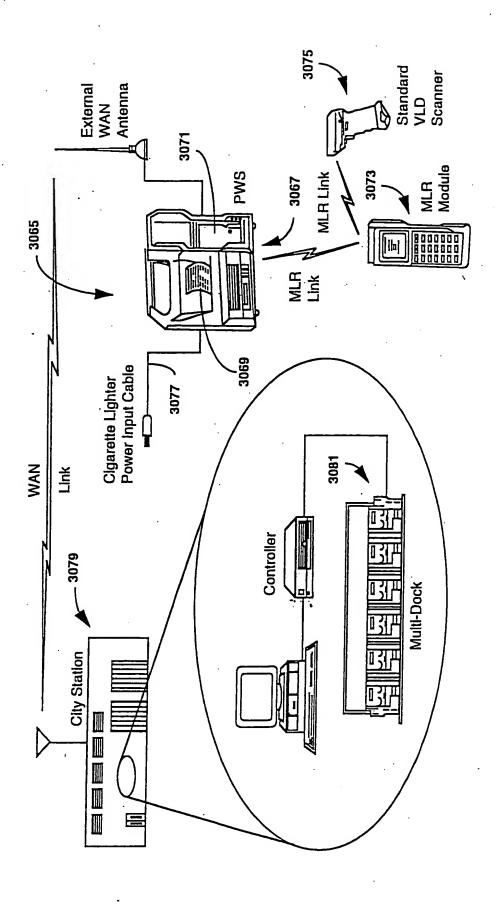
Figure 27

Fig. 28A









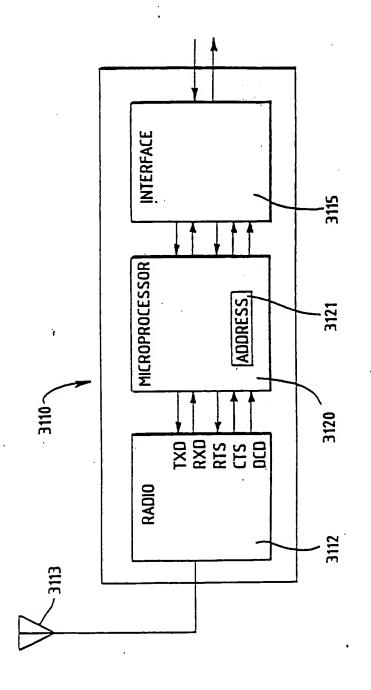


Fig. 30

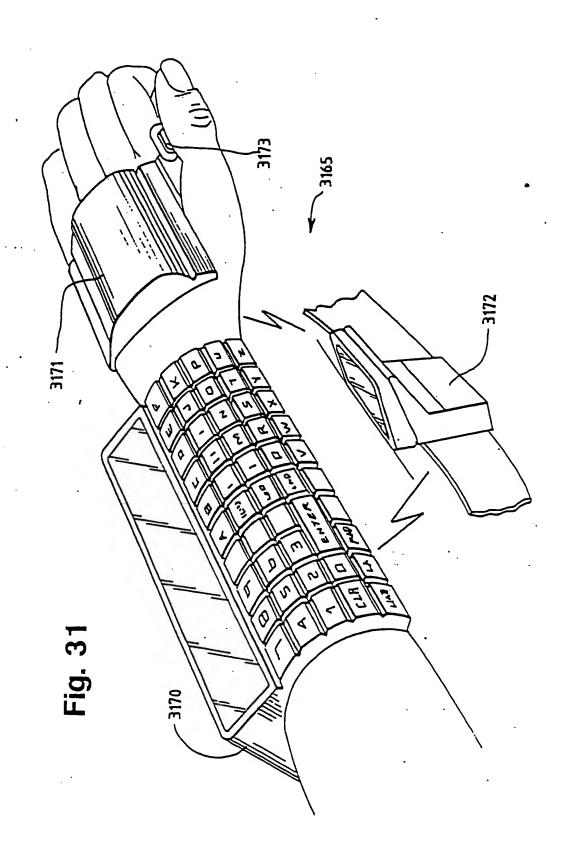
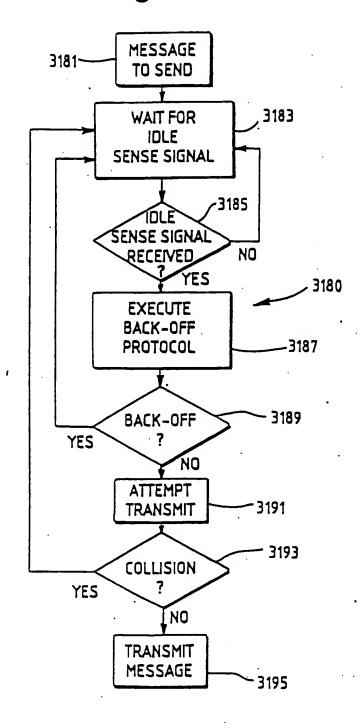


Fig. 32



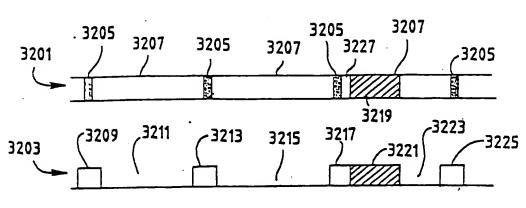


Fig. 33a

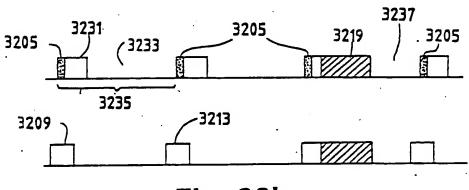
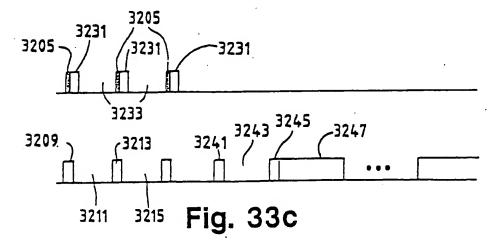


Fig. 33b



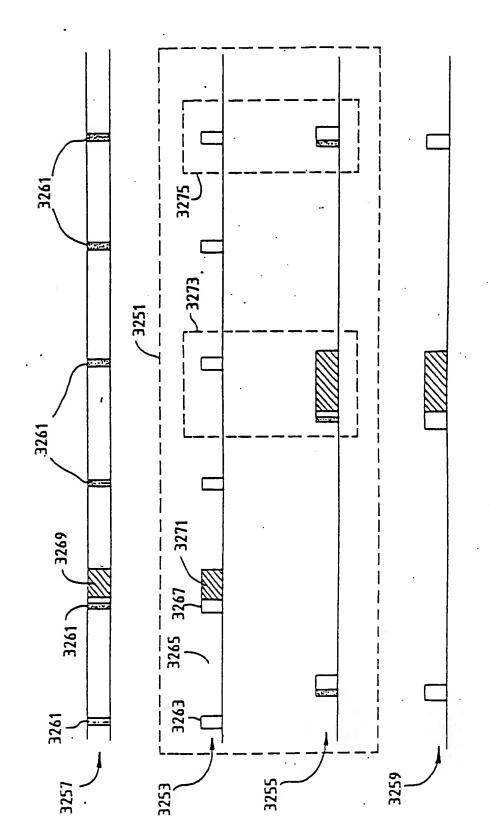


Fig. 34

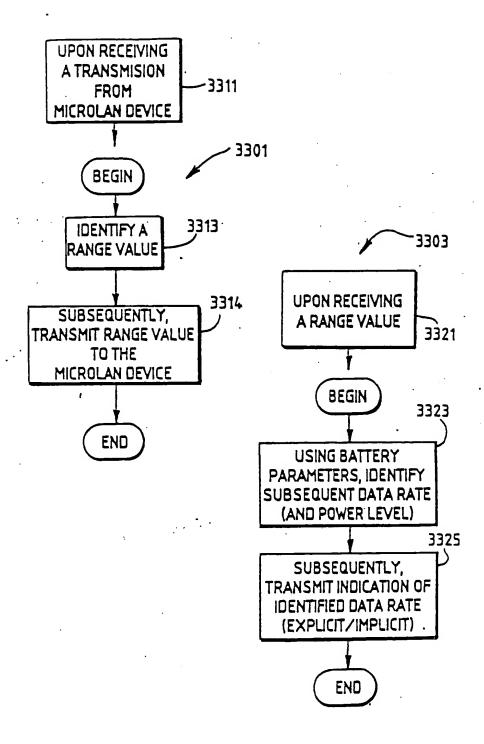
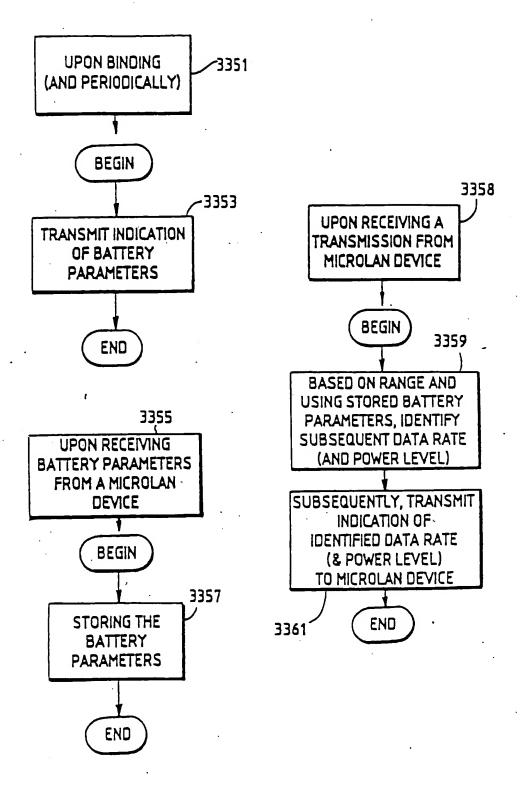


Fig. 35

Fig. 36



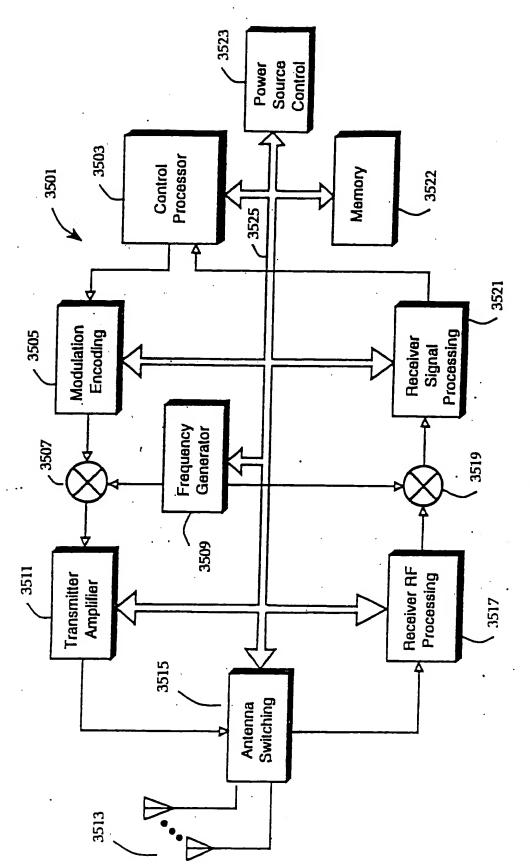
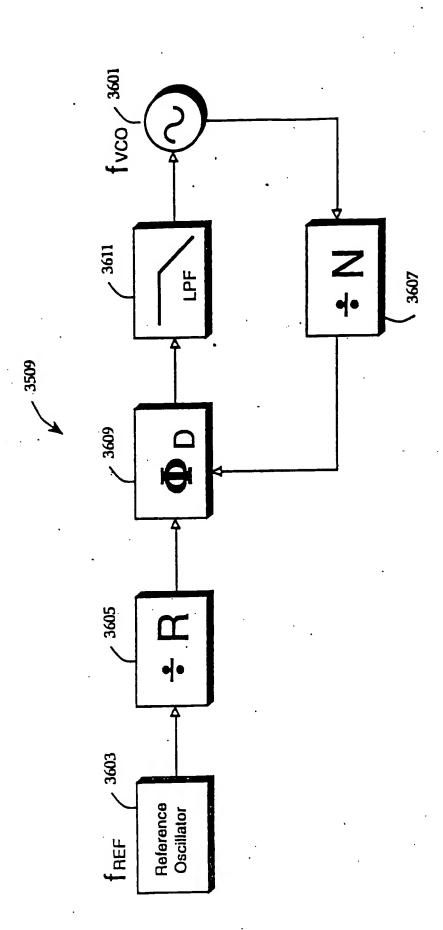


Figure 37



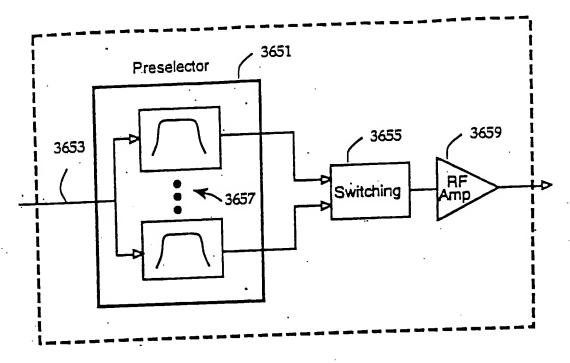


Figure 39

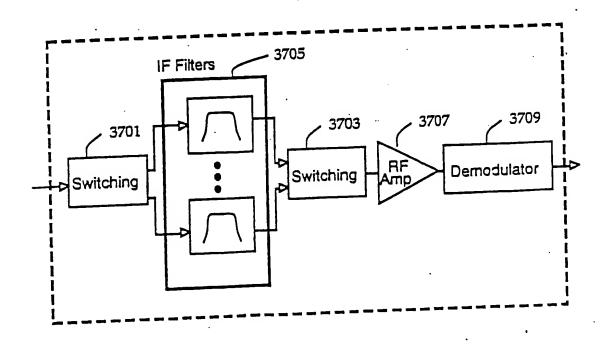


Figure 40

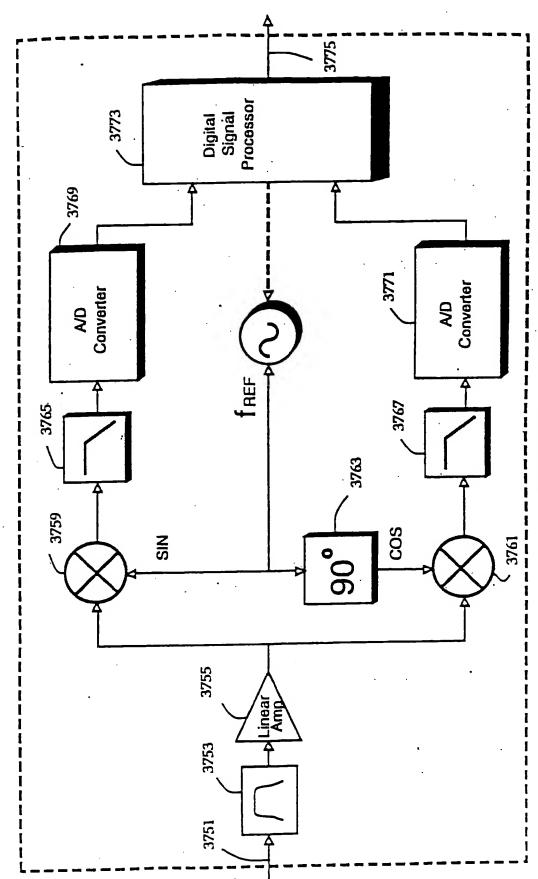


Figure 41

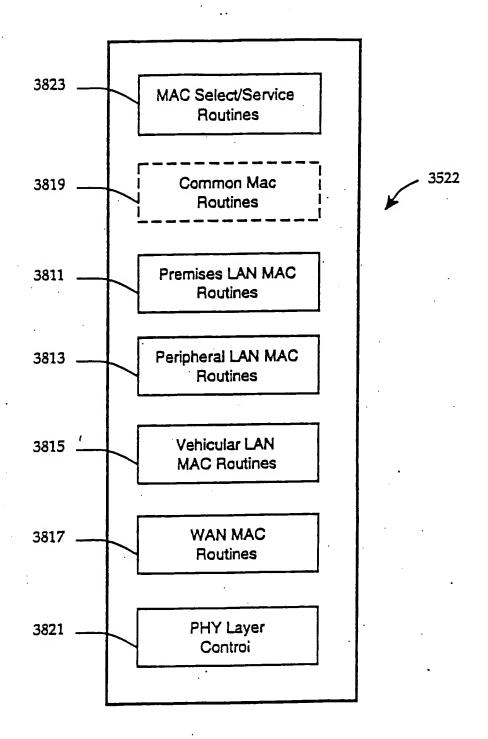
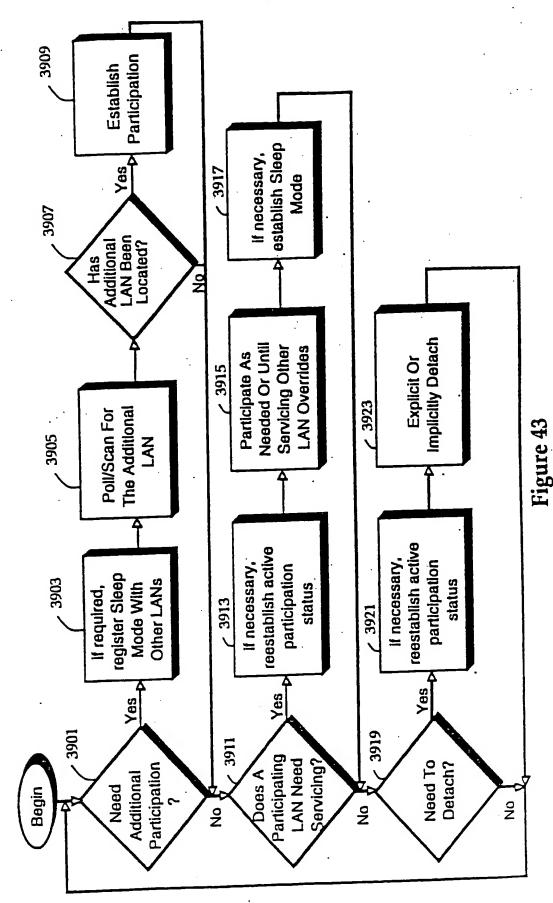


Figure 42



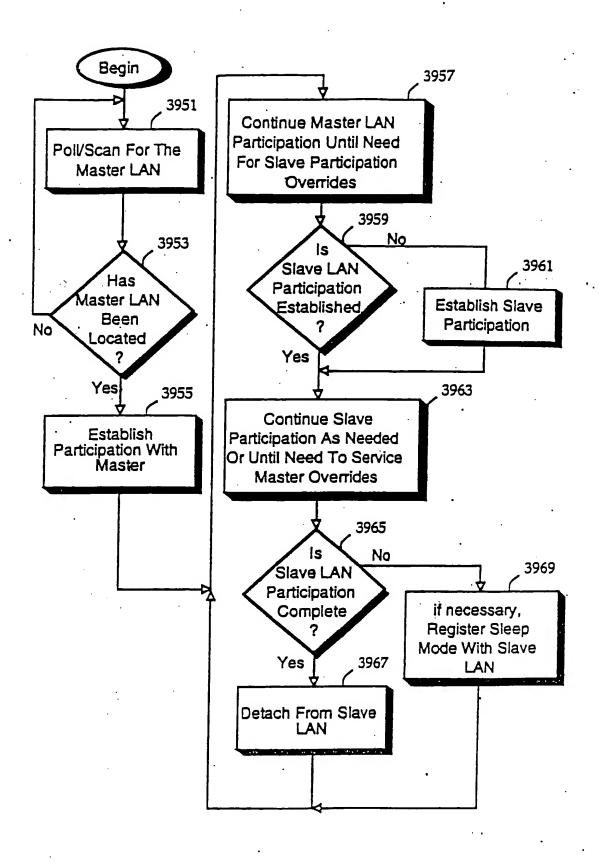


Figure 44

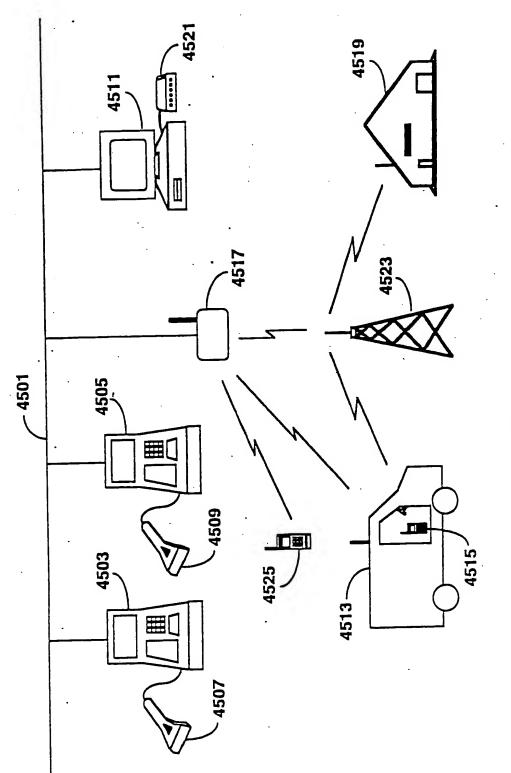


Fig. 45

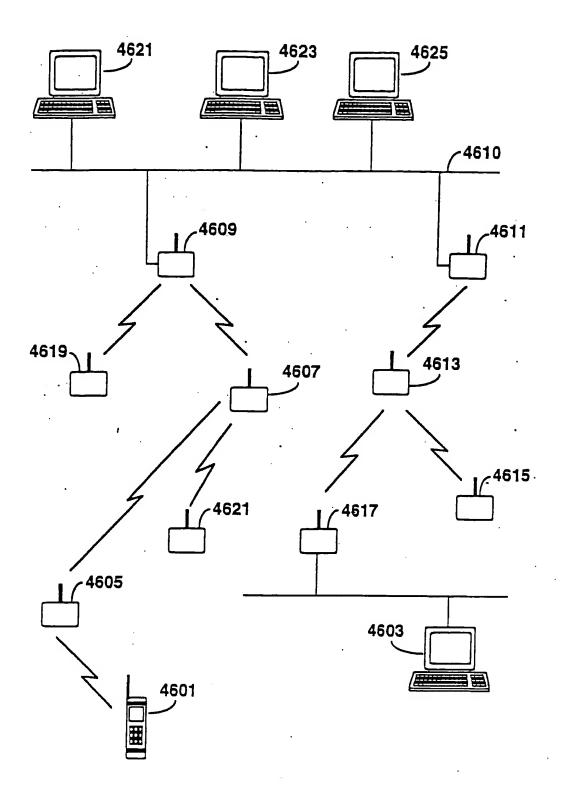


Fig. 46a

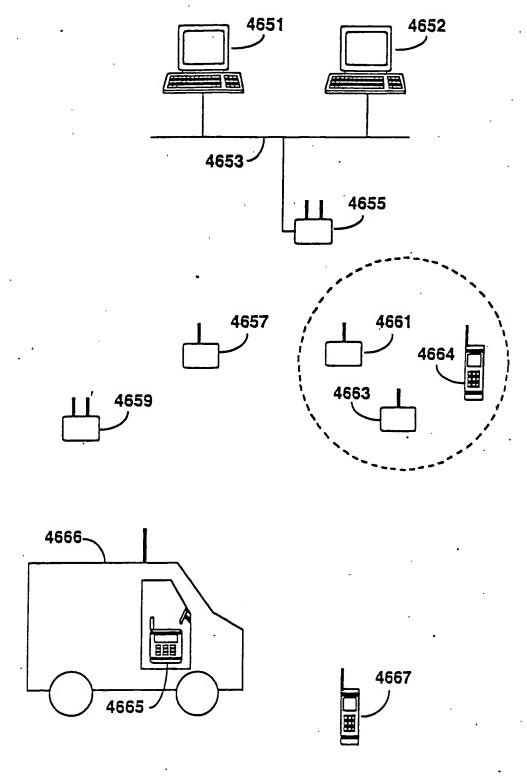


Fig. 46b

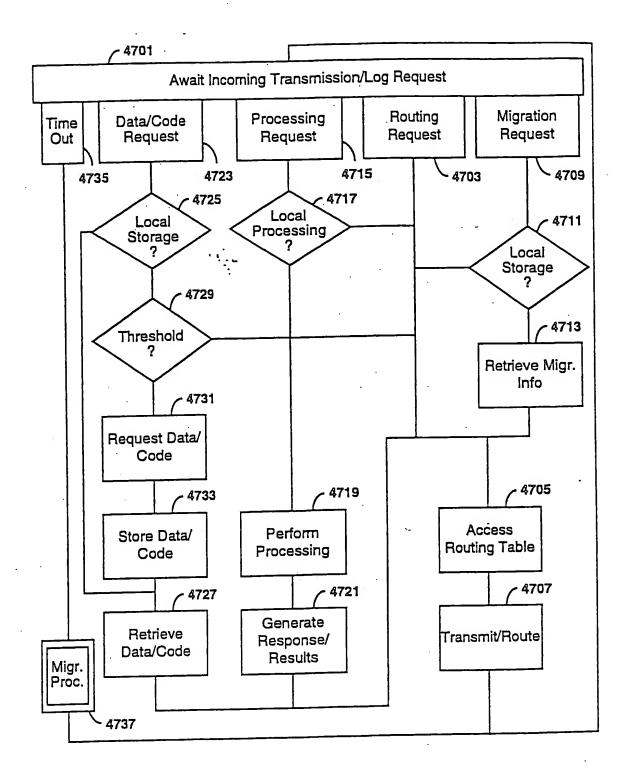


Fig. 47a

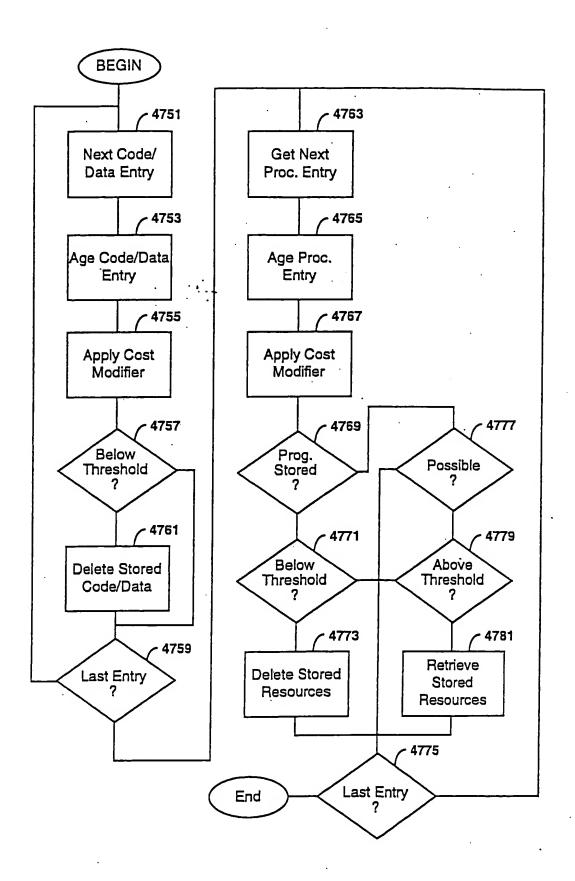


Fig. 47b

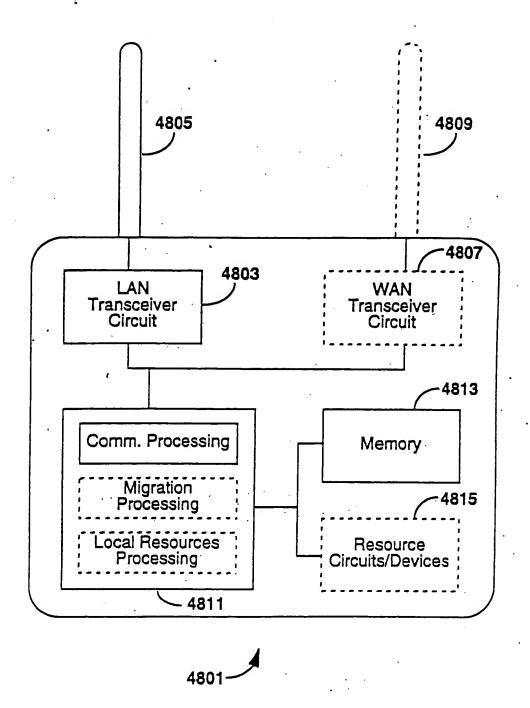


Fig. 48

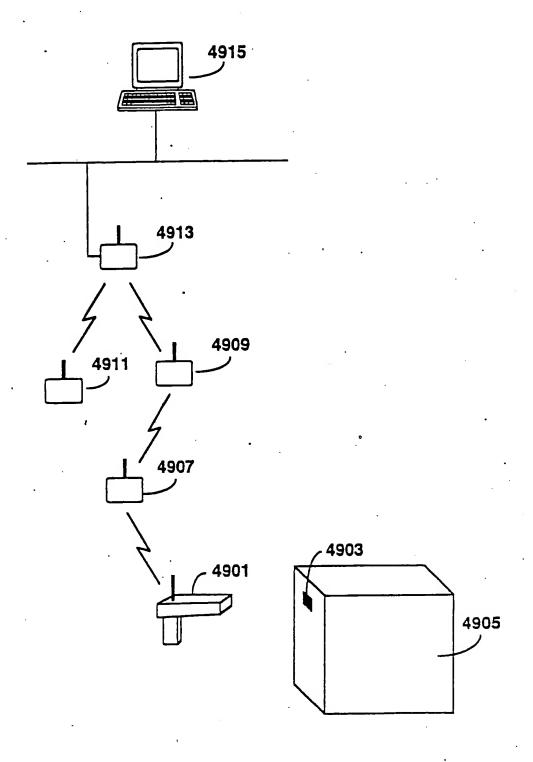


Fig. 49a

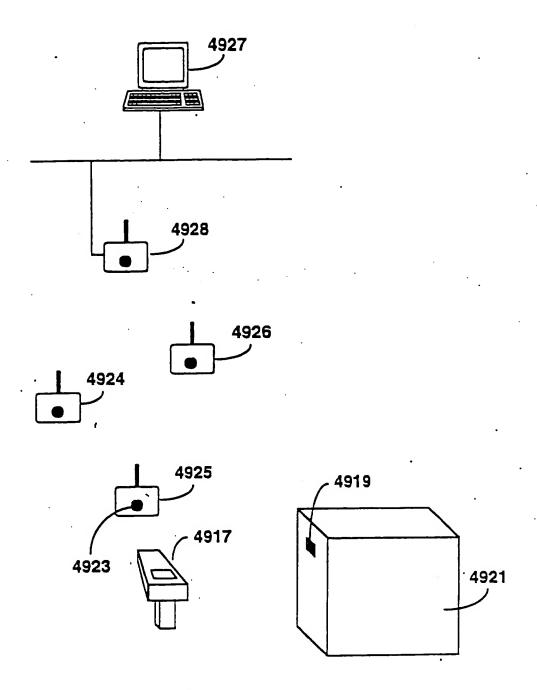


Fig. 49b

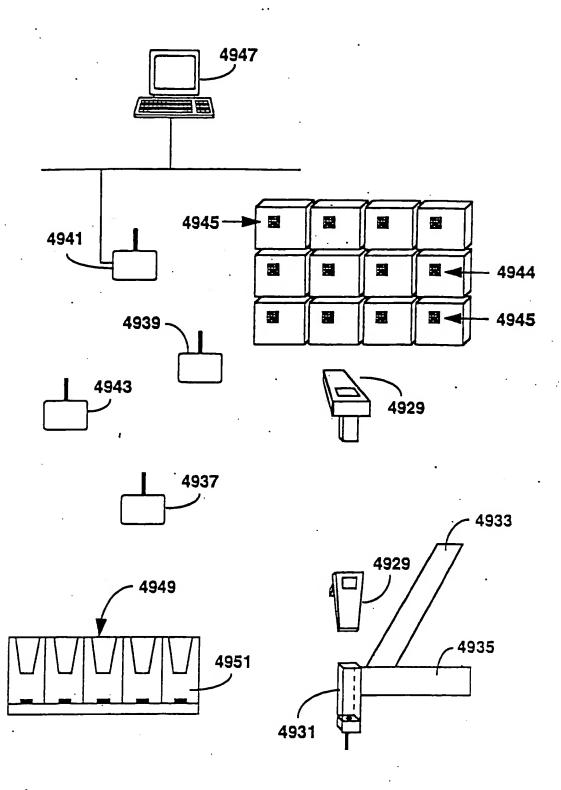


Fig. 49c

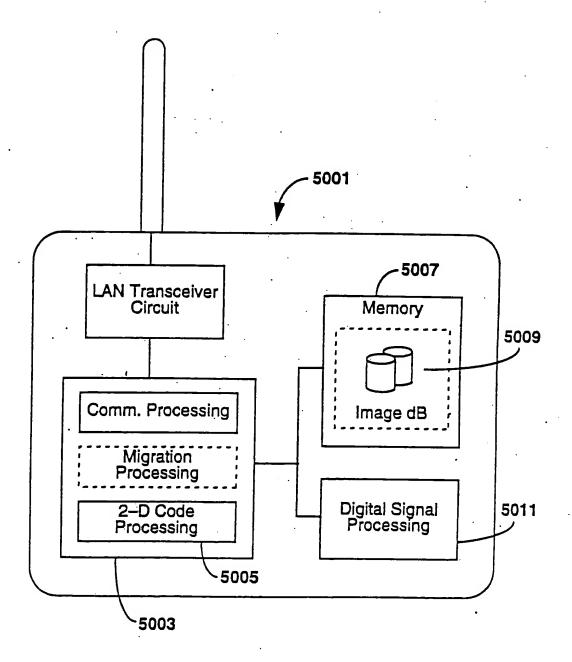


Fig. 50

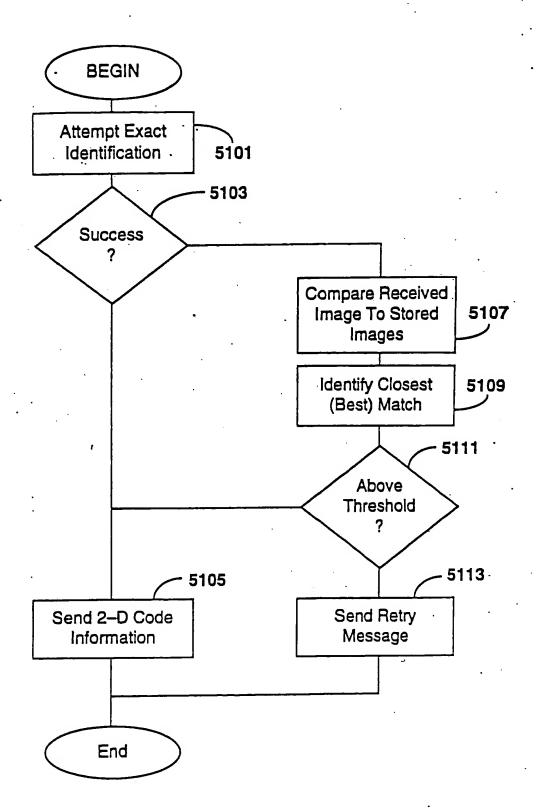


Fig. 51a

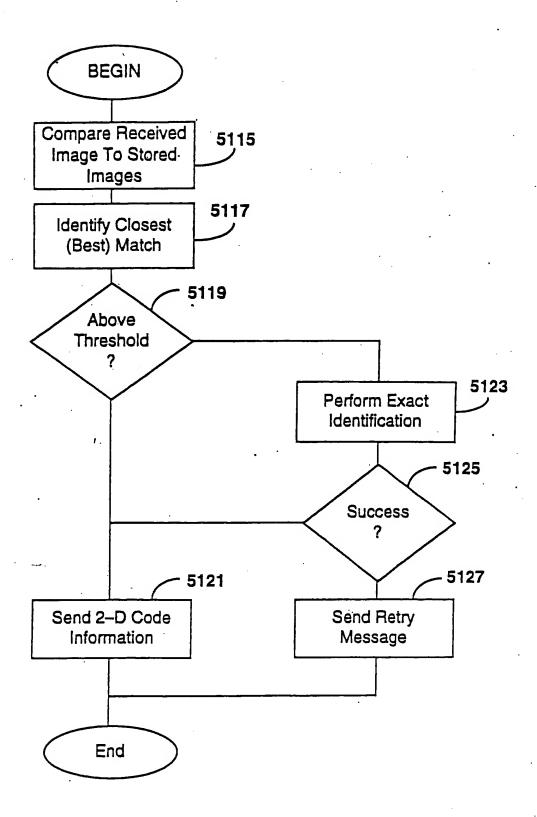
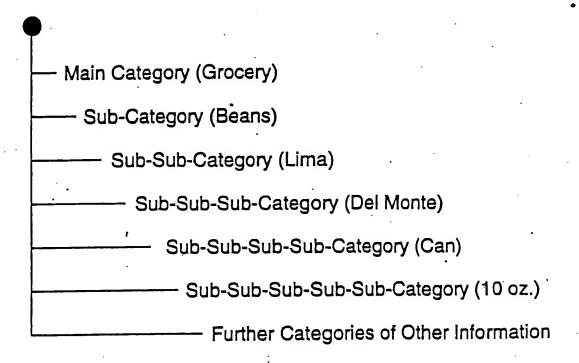


Fig. 51b



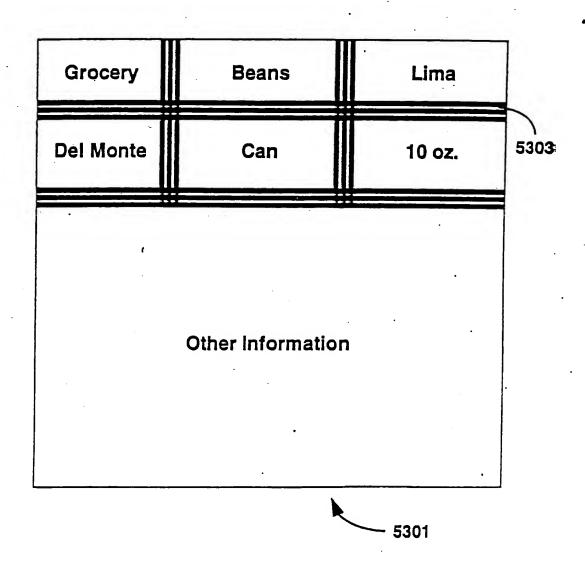


Fig. 53

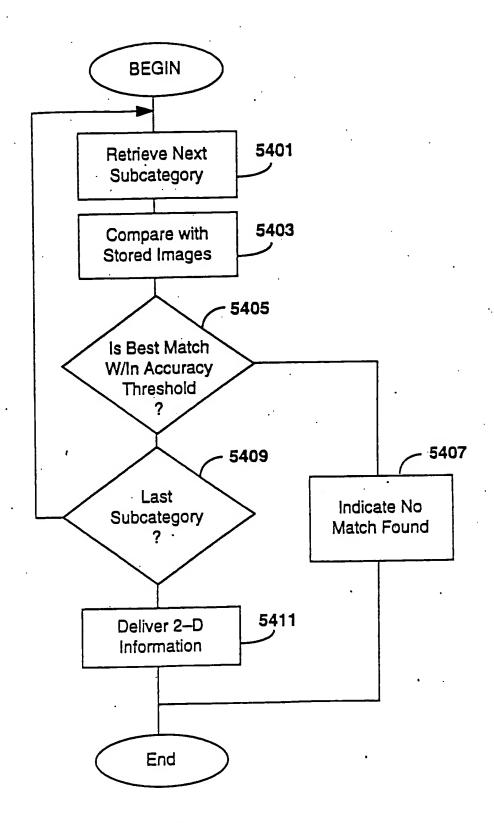


Fig. 54

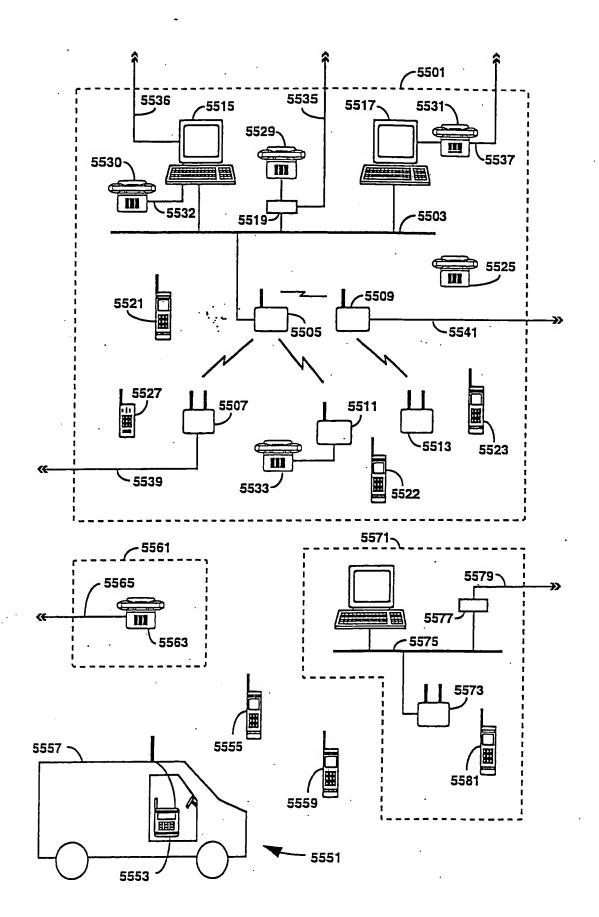
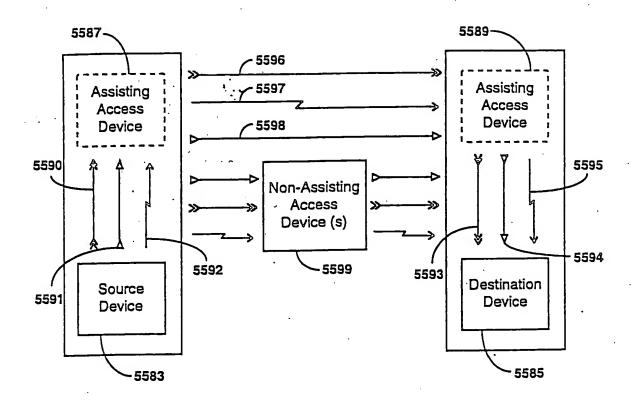


Fig. 55a



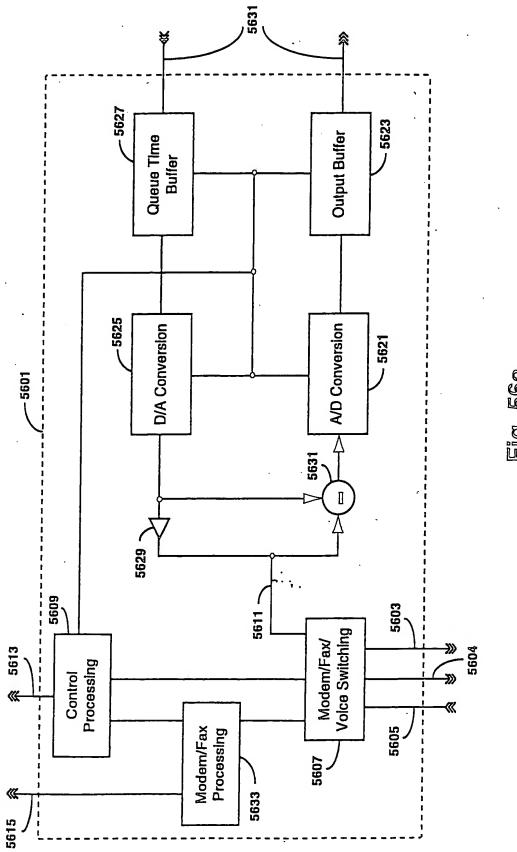


Fig. 56a

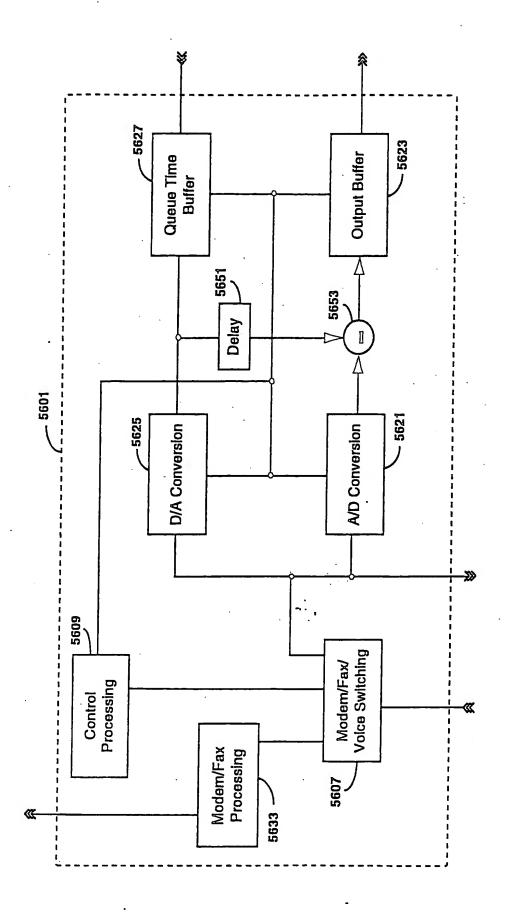


Fig. 56b

Fig. 57

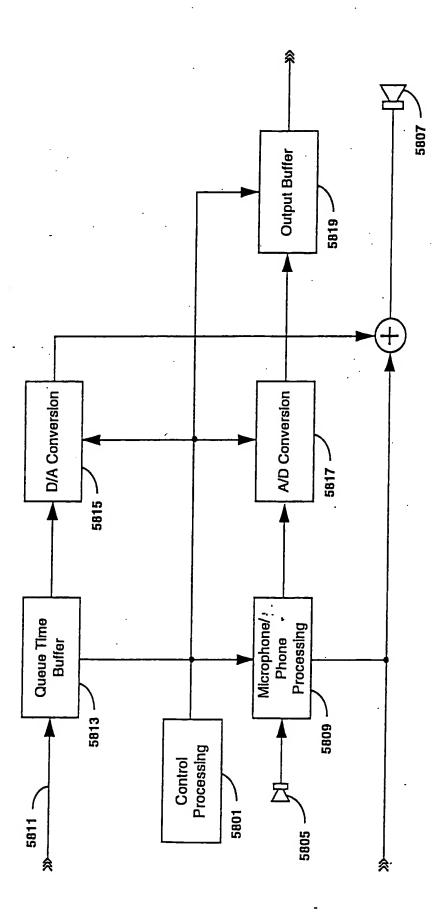


Fig. 58

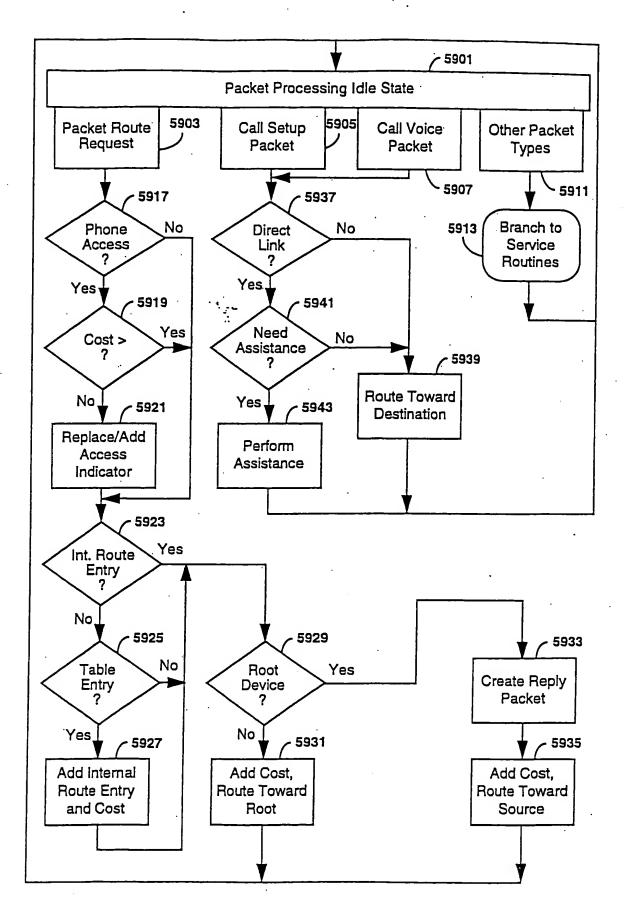


Fig. 59

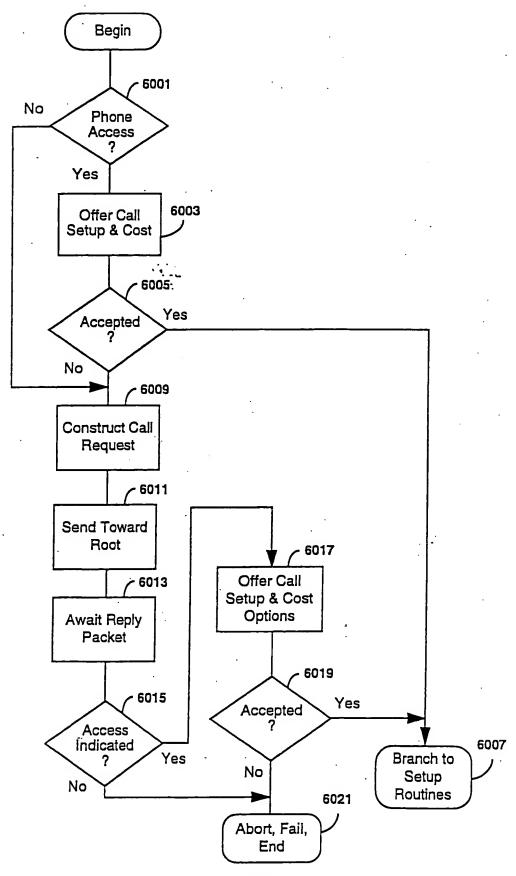


Fig. 60

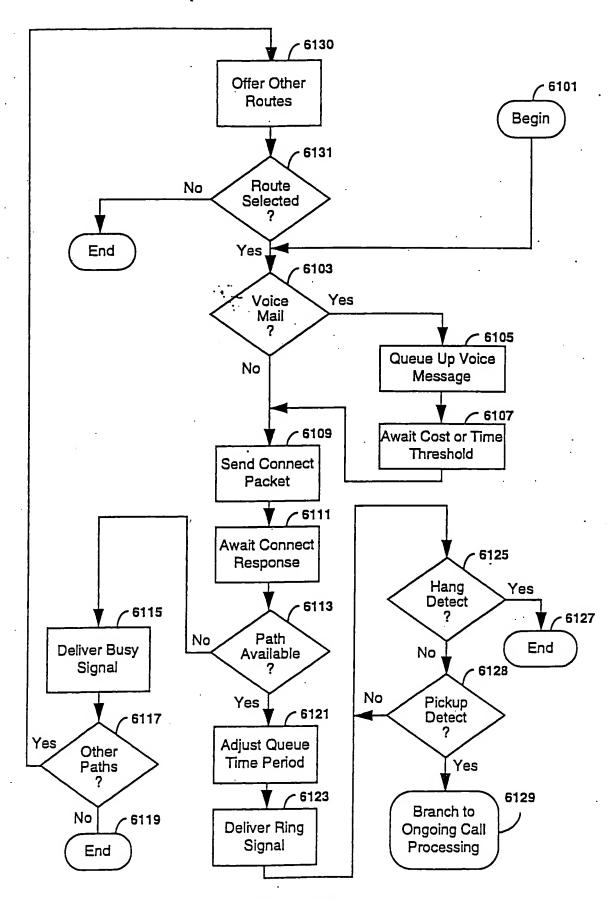


Fig. 61

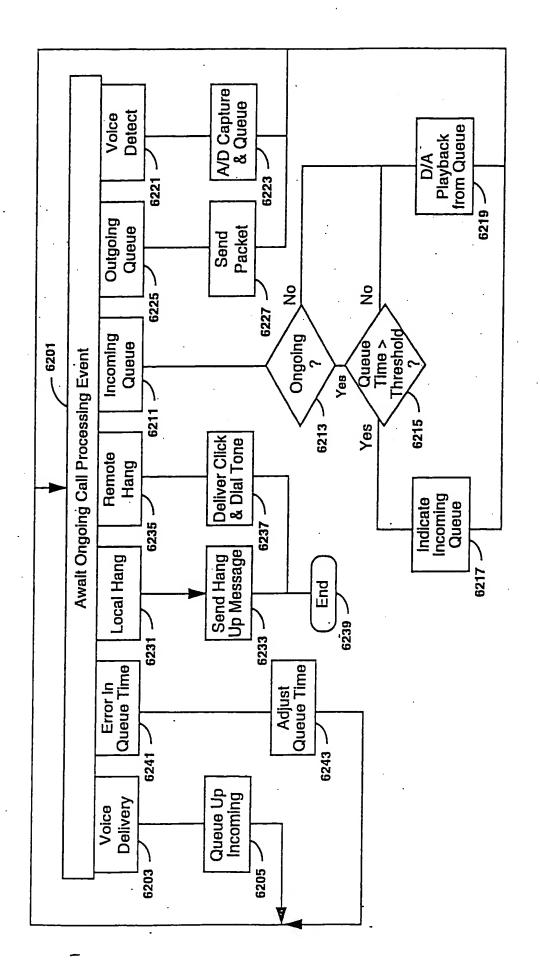


Fig. 62

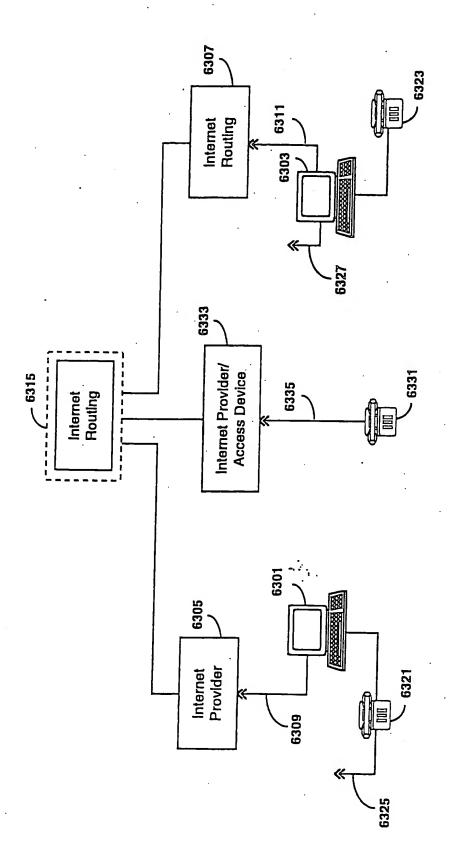


Fig. 63